

## CLAIMS

What is claimed is:

1. A method for producing a switch operating at a low control voltage, the method comprising:
  - forming a plurality of field effect transistors (FETs) connected together in series, wherein the plurality of FETs have six gates therebetween;
  - connecting a first FET to a source voltage source;
  - connecting each gate to a control voltage source; and
  - connecting a last FET to an output.
2. The method of claim 1, further comprising connecting at least one feed forward capacitor to one of the plurality of FETs.
3. The method of claim 2, wherein said connecting at least one feed forward capacitor includes connecting the feed forward capacitor between a gate and either a source or a drain of the one of the plurality of FETs.
4. The method of claim 2, wherein said connecting at least one feed forward capacitor includes connecting a first feed-forward capacitor to a source and a gate of a first FET and a second feed-forward capacitor to a drain and a gate of a last FET.
5. The method of claim 1, further comprising connecting a resistance between at least a subset of the six gates and the control voltage source.

6. The method of claim 1, further comprising connecting a resistance in parallel to at least a subset of the FETs.

7. The method of claim 6, wherein said connecting a resistance in parallel includes connecting a resistor in parallel to all of the plurality of FETs.

8. The method of claim 6, wherein said connecting a resistance in parallel includes connecting a resistor in parallel to each FET of the plurality of FETs.